SKJERVEN
MORRILL
MACPHERSON LLP

Docket No.: M-7928 US

March 14, 2001

Commissioner For Patents
Washington, D. C. 20231

_09/752,304_

Enclosed herewith for filing is a patent application, as follows:

Inventor(s):   Besson, Thierry D.
Title: Implicit Mapping Of Technology Independent Network To Library Cells

X	Return Receipt Postcard
X	This Transmittal Letter (in duplicate)
2	page(s) PTO Form 1449 citing 16 references
X	Copy(ies) of 16 Cited References submitted
X	Information Disclosure Statement (1 page)

☒ The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account 19-2386
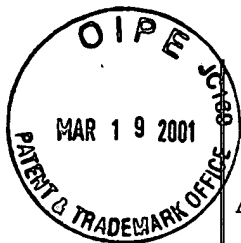
Respectfully submitted,

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on ___March 14___, 2001.

S. Omkar   _March 14, 2001_
Attorney for Applicant(s)   Date of Signature

Omkar K. Suryadevara
Attorney for Applicant(s)
Reg. No. 36,320

734131 v1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):	Besson, Thierry D.
Assignee:	Avant! Corporation
Title:	Implicit Mapping Of Technology Independent Network To Library Cells

Serial No.:	09/752,304	Filing Date:	December 28, 2000
Examiner:	Unknown	Group Art Unit:	Unknown
Docket No.:	M-7928 US		

COMMISSIONER FOR PATENTS Washington, D. C. 20231	San Jose, California March 14, 2001

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR § 1.97(b)

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying PTO Form-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents are enclosed.

Citation of these documents shall not be construed as:

1.   an admission that the documents are necessarily prior art with respect to the instant invention;

2.   a representation that a search has been made, other than as described above ; or

3.   an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on _March 14_, 2001.

S. Omkar   March 14, 2001
Attorney for Applicant(s)   Date of Signature

Respectfully submitted,

S. Omkar

Omkar K. Suryadevara
Attorney for Applicant(s)
Reg. No. 36,320

U.S. Department of Commerce, Patent and Trademark Office	Atty Docket No.	Serial No.
	M-7928 US	09/752,304

INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Applicant(s)	
(Use several sheets if necessary)	Besson, Thierry D.	
	Filing Date	Group
	December 28, 2000	Unknown

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	5,680,332	10/21/1997	Raimi et al	364	578	10/30/1995
	AB	5,748,786	5/5/1998	Ashar et al	364	488	10/28/1994
	AC	5,956,257	9/21/1999	Ginetti et al	17	50	3/31/1993
	AD	6,023,566	2/8/2000	Belkhale et al	395	500.03	4/14/1997
	AE	6,026,222	2/15/2000	Gupta et al	395	500.06	12/23/1997

Foreign Patent Documents

							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AQ	Schlichtmann et al, "Characterization of Boolean Functions for Rapid Matching in EPGA Technology Mapping", 29th ACM/IEEE Design Automation Conference, © 1992, pgs. 374-379
	AR	Hinsberger et al, "Boolean Matching for Large Libraries", © 1998, pgs. 206-211
	AS	Bryant, Randal E., "Graph-Based Algorithms for Boolean Function Manipulation", *IEEE Transactions on Computers,* Vol. C-35, No 8, August 1986, pgs. 677-691
	AT	Brace et al, "Efficient Implementation of a BDD Package", 27th ACM/IEEE Design Automation Conference, © 1990, pgs. 40-44
	AU	"bdd: routine package for logic function manipulation." http://ics.ele.tue.nl/es/research/fv/research/research_bdd.html, December 8, 2000, 3 pgs.
	AV	"BDD Package", http://www.ee.pdx.edu/~cfiles/bdd/, December 8, 2000, 1 pg.
	AW	"BDD Papers, Links, and Related Information", http://www.ee.pdx.edu/~alanmi/research/bdds.htm, December 8, 2000, 7 pgs.
	AX	"EHV BDD Package", http://www.cs.cmu.edu/afs/cs/usr/bwolen/Web/fmcad98/packages/ehv.html, March 9, 2001, 1 pg.

Examiner	Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

U.S. Department of Commerce, Patent and Trademark Office	Atty Docket No.	Serial No.
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	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents

							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL							
	AM							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AY	"Parallel PBF BDD Package", http://www.cs.cmu.edu/afs/cs.cmu.edu/user/bwolen/www/software/ppbf/README, March 9, 2001, pgs.6
	AZ	"Pilgrem Provides Foundry Independence for Integrated Circuit Layouts", http://www.ic-dc.com/pilgrem.html, June 6, 2000, 4 pgs.
	BA	"CAL BDD", http://www-cad.eecs.berkeley.edu/Respep/Research/bdd/cal_bdd/, March 9, 2001, pgs 2

Examiner	Date Considered

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